

FIG. 1
LINE CIRCUIT
20 PER UNIT

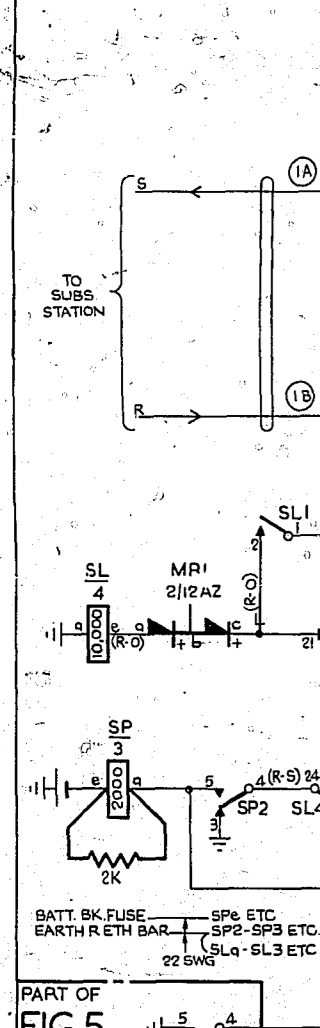


FIG. 2B
FINDER
10 PER UNIT

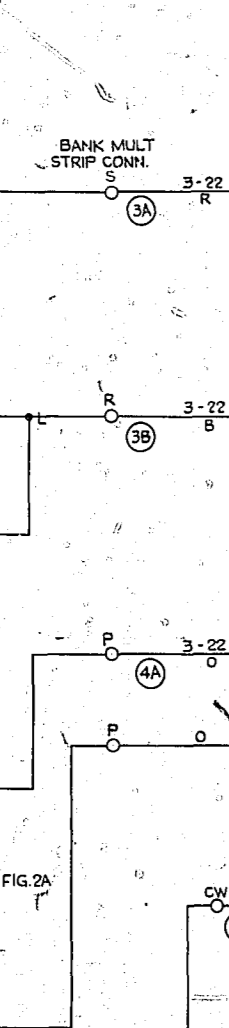
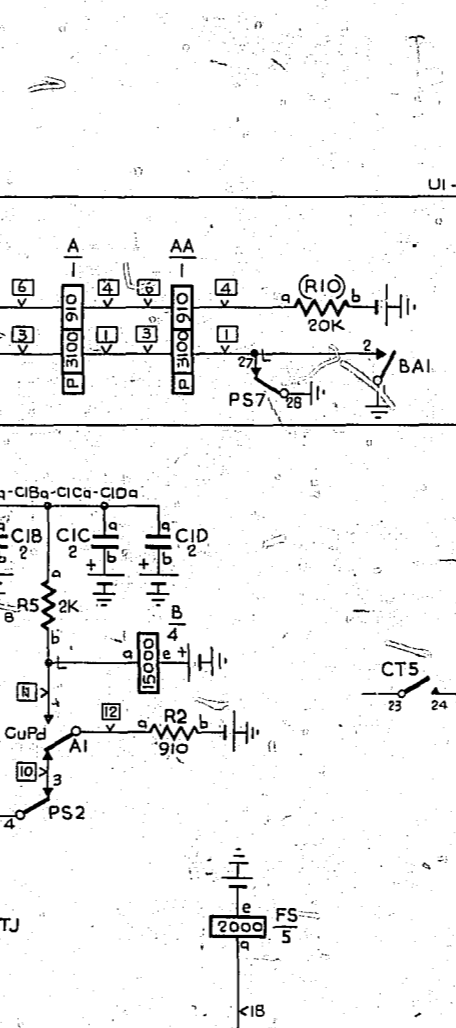


FIG. 2A
JUNCTION RELAY SET
10 PER UNIT



PART OF FIG. 5

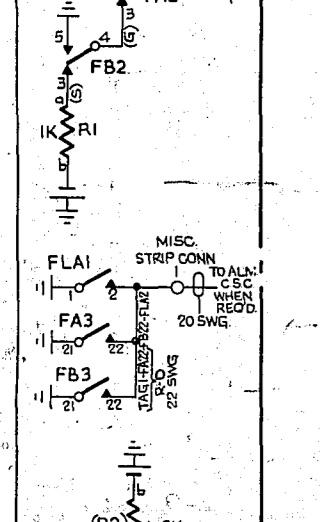


FIG. 3B
ALLOTTER
2 PER UNIT

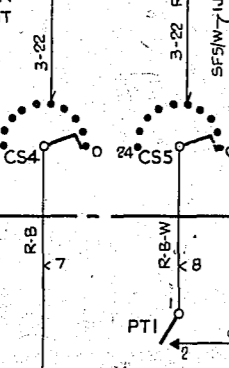


FIG. 3A
ALLOTTER AND CONTROL RELAY SET
2 PER UNIT (MAIN & STANDBY)

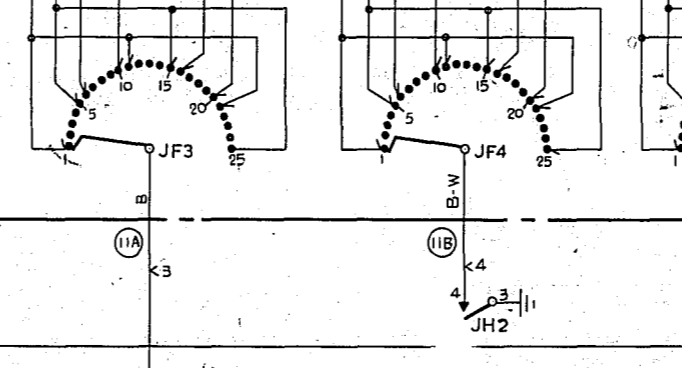


FIG. 5
ALARM CIRCUIT
PER UNIT

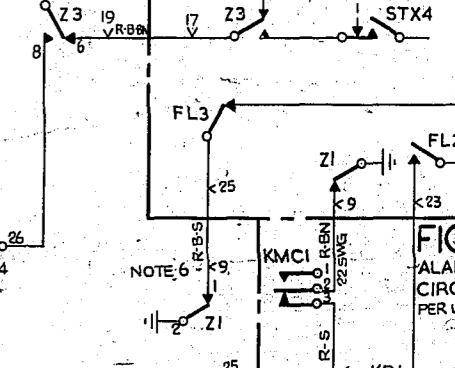


FIG. 4
1 PER FIG. 2A
1 PER FIG. 3B

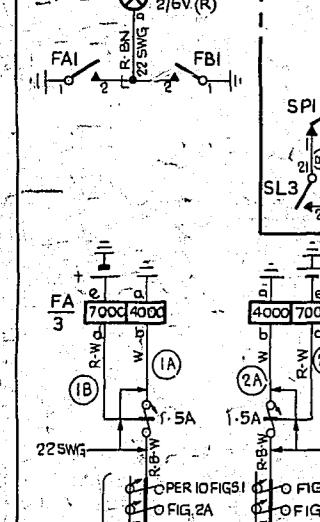
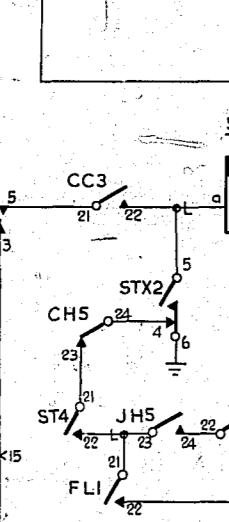


FIG. 6
BATTERY JACKS



- NOTES
- FUSING
 - FIG. 1 - 80V 1.5A PER 10 CCTS.
 - FIG. 2A - 80V 1.5A PER CCT
 - FIG. 2B - 80V 1.5A PER CCT
 - FIG. 3A - 80V 1.5A PER CCT
 - FIG. 3B - 80V 1.5A PER CCT
 - FIG. 4 - 80V 1.5A PER 5 CCTS.
 - FIG. 5 - 80V 1.5A PER 5 CCTS.
 - FIG. 6 - 80V 1.5A PER 5A RELAY
 - RELAYS FL 2, X & Y AND IG & IGA ARE TWIN TYPE
 - THE COILS OF RELAY CD ARE DIFFERENTIALLY CONNECTED
 - PULSE GENERATOR PS IS TO BE ADJUSTED TO GIVE RELAY P A 30 MS MAKE AND BREAK PERIOD
 - CI TO BE ADJUSTED TO GIVE RELAY STX A RELEASE LAG GREATER THAN THE MAXIMUM SETTING UP TIME I.E. 1.75 TO 2 SECS.
 - SHLEF JACK U POINTS 9 & 11 AND 14 & 16 FIG. 2A AND 9 & 11, 13 & 15 AND 17 & 19 FIG. 3A MAKE CONTACT WHEN RELAY SET IS REMOVED
 - ALL WIRING TO BE 25 SWG UNLESS OTHERWISE SPEC'D. OUTLETS 1-5 OF U.F. ARE 1-6 TO BE WIRED IN A SIMILAR MANNER TO THAT SHOWN FOR OUTLET 1 OF ARCS 1 AND 2
 - CI TO BE ADJUSTED TO GIVE RELAY B A RELEASE LAG OF 325-475 ms SECS.

FIG. 2A

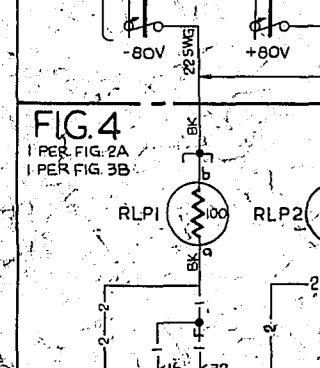


FIG. 3A

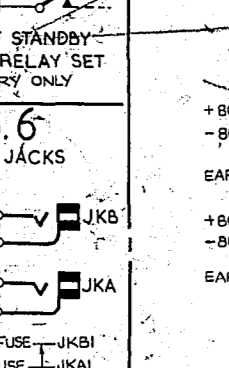
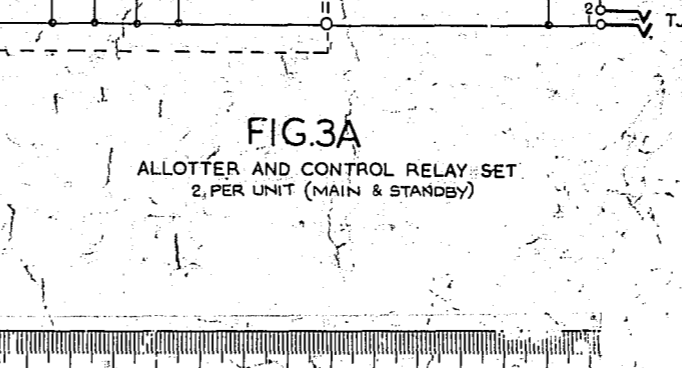


FIG. 3B



AMENDMENT PARTICULARS	ISSUE DATE	APPROVED	SIZE	BOV	IE