

PART OF  
GBW 17480 OR EQUIV.  
EXPLANATORY ONLY

FROM  
20 STATE  
CHARACTER SELECTION  
BINARY CIRCUIT  
GBW 17500 OR  
EQUIV.

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FROM  
CHARACTER CODE FORMING &  
SELECTION CIRCUIT  
GBW 17510 OR EQUIV.

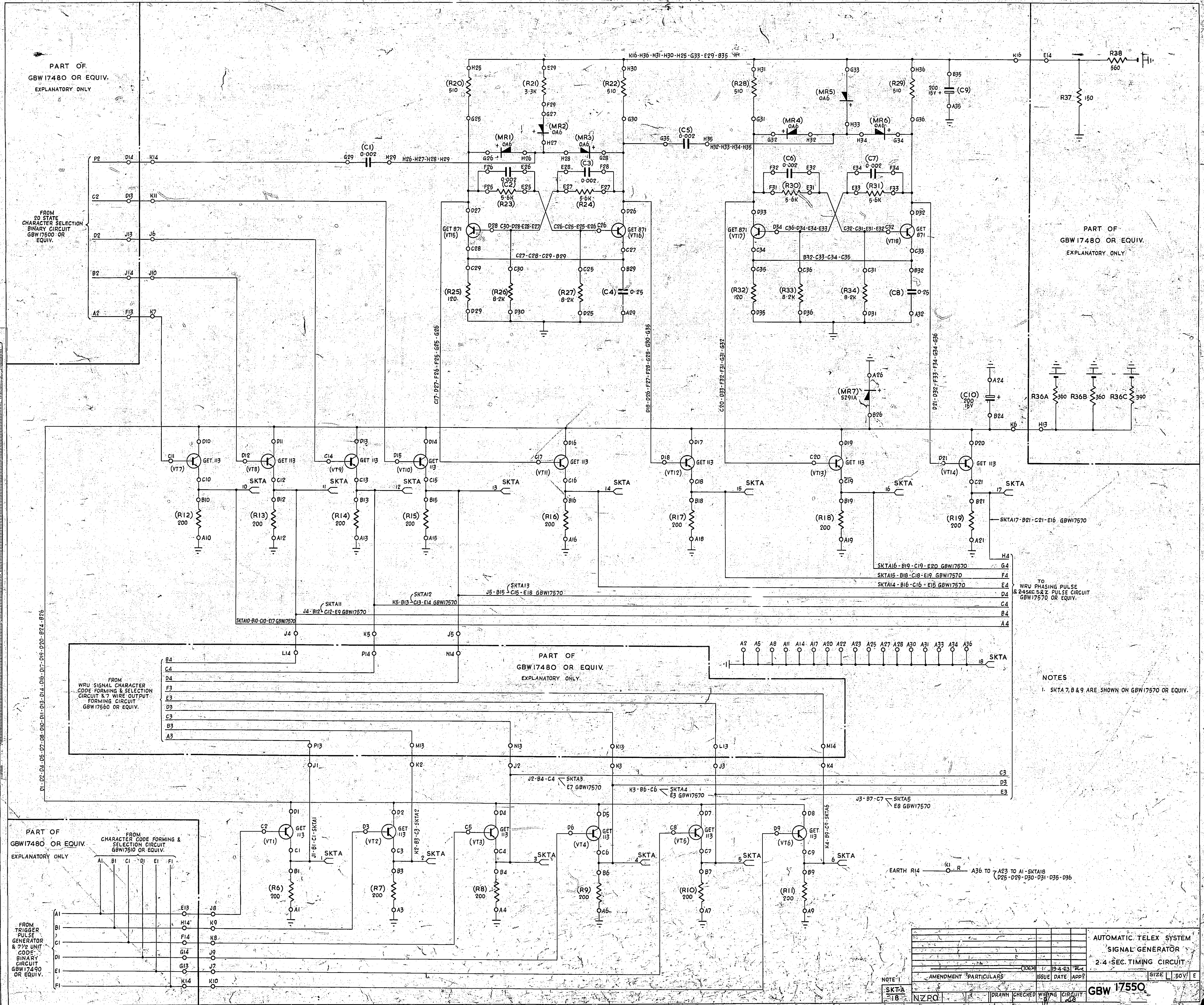
FROM  
TRIGGER PULSE  
GENERATOR & 7 1/2 UNIT  
CODE  
BINARY  
CIRCUIT  
GBW 17400  
OR EQUIV.

TO  
WRU PHASING PULSE  
& 2.4 SEC. S&Z PULSE CIRCUIT  
GBW 17570 OR EQUIV.

NOTES  
1. SKTA 7, 8 & 9 ARE SHOWN ON GBW 17570 OR EQUIV.

AUTOMATIC TELEEX SYSTEM			
SIGNAL GENERATOR			
2.4 SEC. TIMING CIRCUIT			
LOG	ISSUE DATE	APP	SIZE
AMENDMENT PARTICULARS	ISSUE DATE	APP	SIZE
NOTE 1			GBW 17550
SKTA	NZPO	DRAWN	CHECKED
18		WIRING	CIRCUIT

0  
5  
10  
15  
20  
25  
30  
35  
40  
45  
50  
55  
60  
65  
70  
75  
80  
85  
90  
95  
100



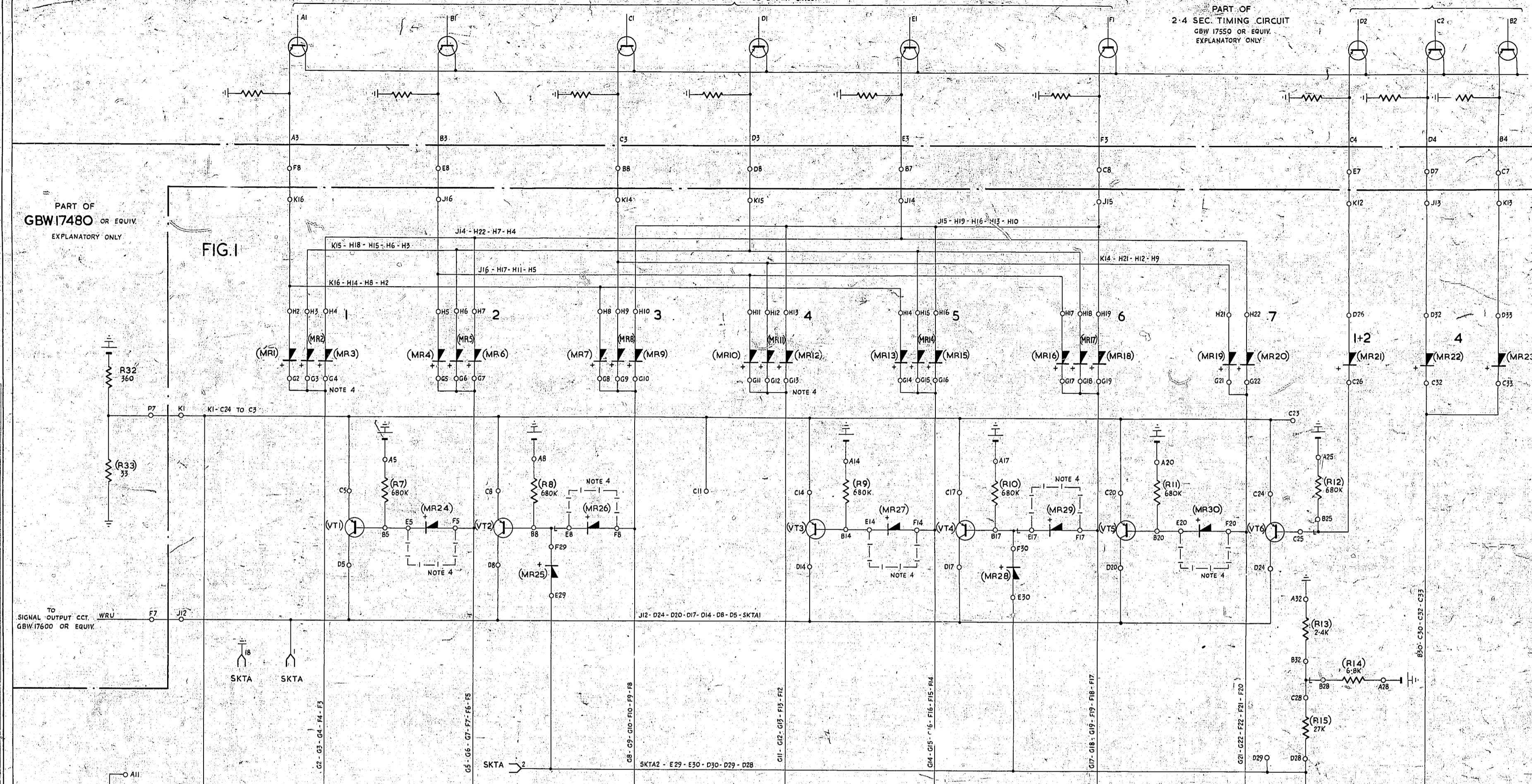
FROM 7 1/2 UNIT CODE BINARY CIRCUIT

FROM 20 STATE BINARY CIRCUIT

PART OF  
2-4 SEC. TIMING CIRCUIT  
GBW 17550 OR EQUIV.  
EXPLANATORY ONLY

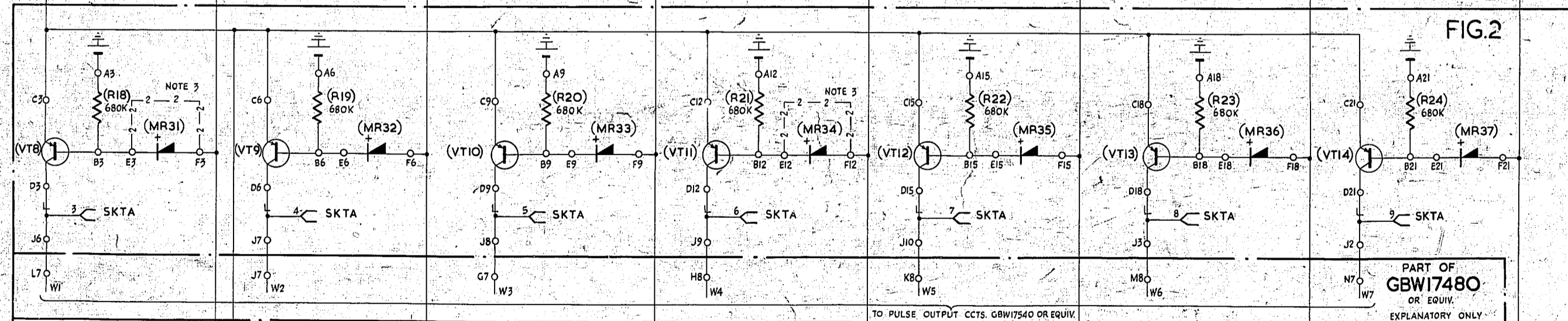
PART OF  
GBW17480 OR EQUIV.  
EXPLANATORY ONLY

FIG. 1



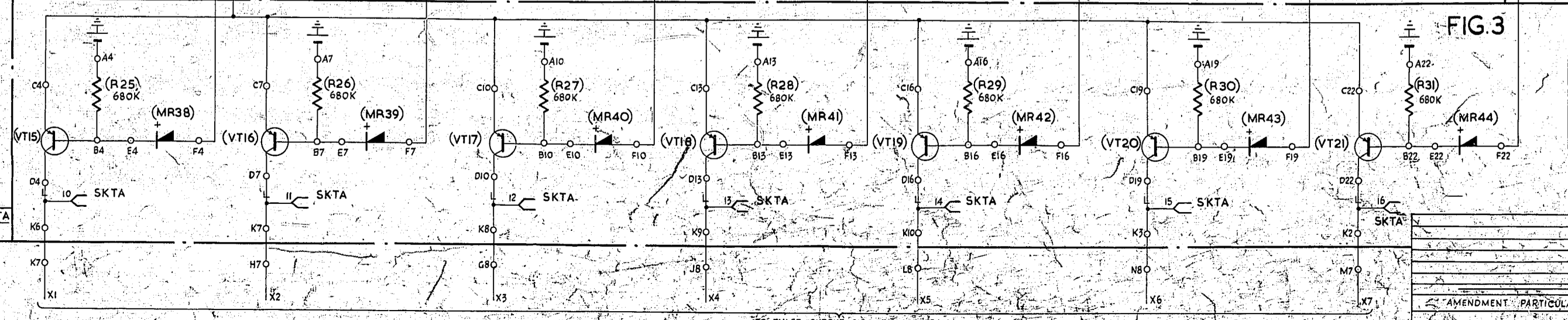
0  
5  
10  
15  
20  
25

FIG. 2



PART OF  
GBW17480  
OR EQUIV.  
EXPLANATORY ONLY

FIG. 3



PART OF  
GBW17480 OR EQUIV.  
EXPLANATORY ONLY

- NOTES -
1. TRANSISTORS ARE TYPE GET 102 UNLESS OTHERWISE SPECIFIED
  2. DIODES ARE MULLARD TYPE OA6
  3. THIS DIAGRAM SHOWS TWO 7-WIRE OUTPUT CIRCUITS (FIGS. 2 & 3). WHEN ONLY ONE 7-WIRE OUTPUT CIRCUIT IS REQUIRED, OMIT FIG. 2 AND DIODES MR31 & 34 AND ADD STRAPS SHOWN - 2-2 -
  4. WHEN NO 7-WIRE OUTPUT CIRCUITS ARE REQUIRED OMIT FIGS. 2 & 3 AND DIODES MR1, 2, 3, 10, 11, 12, 24, 26, 27, 29 & 30 AND ADD STRAPS SHOWN - 1 -
  5. WHEN ONLY PART OF 7-WIRE OUTPUT IS REQUIRED, DELETE STAGES NOT REQUIRED AND MODIFY AS PER NOTE 3 OR 4 FOR THE DELETED STAGES

AUTOMATIC TELEX SYSTEM -  
SIGNAL GENERATOR  
WRU SIGNAL CHARACTER CODE  
FORMING & SELECTION CIRCUIT &  
7-WIRE OUTPUT FORMING CIRCUIT