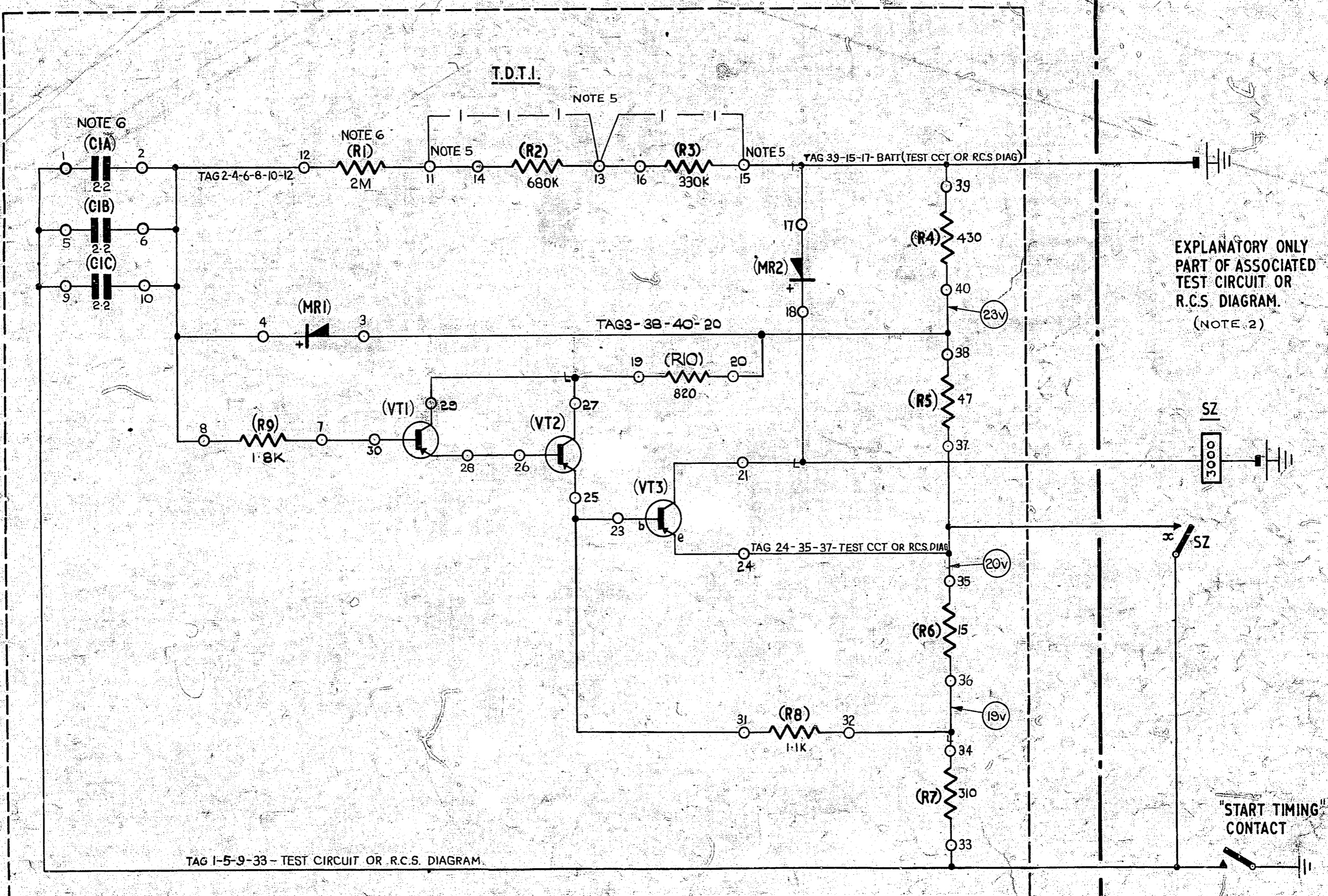


5 Centimetres
10
15
20
25
30



EXPLANATORY ONLY
PART OF ASSOCIATED
TEST CIRCUIT OR
R.C.S. DIAGRAM.
(NOTE 2)

"START TIMING"
CONTACT

NOTES.

1. FUSED WITH ASSOCIATED TEST CCT OR R.C.S. DIAGRAM.
2. FOR TYPICAL APPLICATION SEE GBW 16720 FIG.1. OR EQUIV. OR GBW 13730 FIG.31B OR EQUIV.
3. ALL WIRING TO BE 25 S.W.G. UNLESS OTHERWISE SPECIFIED. STRAPS BETWEEN ADJACENT TAGS TO BE IN B.T.C.
4. THIS CIRCUIT IS ARRANGED TO PROVIDE A DELAY OF 10 SECONDS PRIOR TO THE OPERATION OF RELAY SZ.
5. TO ADJUST THE DELAY PERIOD WITHIN THE LIMITS OF 9-11 SECONDS ADD STRAPS-1-1- AS REQUIRED.
6. ON SOME EARLY EQUIPMENT THE VALUE OF CAPACITORS CIA-CIC WAS 2 μ F & RESISTOR R1 WAS 2.2 M.
7. FIGURES SHOWN THUS (19v) ARE APPROXIMATE DC VOLTAGES WITHIN 10 SECS. OF CLOSING THE START CONTACTS.

TRANSISTOR DELAY TIMER			
FOR USE WITH I.V.F. LINE CIRCUITS			
N.Z. S.S.A.C. Nos 2T & 3T			
AMENDMENT PARTICULARS	ISSUE	DATE	APPD
NOTES 6 & 7 ADDED.	(39542)	4	22-7-71 B.S.H.
R10 AMENDED SEMI-CONDUCTOR CODES REMOVED (83879)	(53392)	3	10-8-67 JTB
VALUE OF R9 ROUTING & TITLE AMENDED, R10 ADDED.	(47273)	2	1-8-68 JTB
		1	12-2-65 JTB
REF: I.V.F. SIZE S 50V S			
GBW 16730			
N.Z.P.O.	DRAWN	CHECKED	WIRING CIRCUIT
	B.M.G.	JTB	A.S.H.