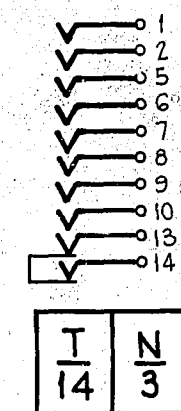


NOTES: 1. FUSING: - 50V: 1.5A PER SELECTOR: +50V: 0.5A PER 3 SELECTOR.
 2. LEAD 'E' NOT REQ'D: WHEN GCT. IS DIRECTLY CONNECTED TO GBW 10480 OR EQUIV.
 3. P.G. ALARM BATT. ONLY REQ'D: WHEN SELECTOR TERMINATES A JUNCTION.
 4. LEADS T1 & T2 ONLY REQ'D: WHEN NO LOCAL ACCESS TO TEST SELECTOR IS PROVIDED.
 5. ∅ FOR WIRING SEE GBW 10640 OR EQUIV.
 6. WHEN CIRCUIT IS PRECEDED BY TEST INTERMEDIATE SELECTOR, UC LEADS ARE CONNECTED AS FOLLOWS: - & + (TEST), E - & + (OPERATE), RL FROM - & +, P.V.A.Z. RESPECTIVELY OF INTERMEDIATE SELR. IN THIS CASE ADD STRAP BETWEEN U3 & U5.

BOOSTER BATT (W) U4-RAb-RAc
 BATT. (W) U12-RLc-F5c-EAe-RCc-He-PTAe-YAb-YBb-Ab-PTe-Ee-PTBe-
 Ve-Re.
 EARTH. (R) U11-RL23-RA21-C2-H2G-Ae-B21-B23-B1-B5-PTZ-
 PCa-NG-QBe
 POS. BATT. (W) U14-YDa-YEa



TEST SELECTOR

SLOW RELEASE SLOW OPERATE 50V E

DRN: 5-5 CKD ORIGIN: 5-5
 TCD: N.5. APPD STANDARD: ATW 22001

NZPO ENGINEER-IN-CHIEF, WELLINGTON

SHT 1 of 1 GBW 10460
 SIZE A2 MOD A

C 25-1-84	G.U. AJR	42366	IAA	GG	OHMS FOR RESISTORS	YVE	AMD.
B 15-9-82	G.U. GJ	0080693	GRH	JR	MINOR	AMD	
A 10-6-81	N.S. AJR	70479	DH	JR	RETRACTED MANUFACTURE	10151242	
ISS	DATE	DEL	DC	ORDER	CKD	APPD	CHANGE

